

E

8063 Errata

E.1 INTRODUCTION

This errata sheet corrects pin number assignment errors in the 8063 Rev 0 Manual.

E.2 PIN ERRORS

On page 2-20, Table 2-4 signal CH2 should be on pin 41 and signal CH22 should be on pin 13. The corrected Table 2-4 is shown on page E-2 of this errata.

Pages 2-28 and 2-29 have incorrect pin numbers. The correct pages are on pages E-3 and E-4 of this errata.

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05/01/09

TABLE 2-4 8063 DIGITAL SIGNAL-PIN ASSIGNMENTS

Signal	Signal Weighting		Pin	User Signals	
	Binary	BCD/HEX		Pin	Signal
CH 8	Byte 1 Bit 7	MSN Bit 8	39		
CH 7	Byte 1 Bit 6	MSN Bit 4	18		
CH 6	Byte 1 Bit 5	MSN Bit 2	60		
CH 5	Byte 1 Bit 4	MSN Bit 1	40		
CH 4	Byte 1 Bit 3	MSN-1 Bit 8	19		
CH 3	Byte 1 Bit 2	MSN-1 Bit 4	61		
CH 2	Byte 1 Bit 1	MSN-1 Bit 2	41		
CH 1	Byte 1 Bit 0	MSN-1 Bit 1	20		
CH 16	Byte 2 Bit 7	MSN-2 Bit 8	15		
CH 15	Byte 2 Bit 6	MSN-2 Bit 4	57		
CH 14	Byte 2 Bit 5	MSN-2 Bit 2	37		
CH 13	Byte 2 Bit 4	MSN-2 Bit 1	16		
CH 12	Byte 2 Bit 3	MSN-3 Bit 8	58		
CH 11	Byte 2 Bit 2	MSN-3 Bit 4	38		
CH 10	Byte 2 Bit 1	MSN-3 Bit 2	17		
CH 9	Byte 2 Bit 0	MSN-3 Bit 1	59		
CH 24	Byte 3 Bit 7	MSN-4 Bit 8	54		
CH 23	Byte 3 Bit 6	MSN-4 Bit 4	34		
CH 22	Byte 3 Bit 5	MSN-4 Bit 2	13		
CH 21	Byte 3 Bit 4	MSN-4 Bit 1	55		
CH 20	Byte 3 Bit 3	MSN-5 Bit 8	35		
CH 19	Byte 3 Bit 2	MSN-5 Bit 4	14		
CH 18	Byte 3 Bit 1	MSN-5 Bit 2	56		
CH 17	Byte 3 Bit 0	MSN-5 Bit 1	36		
CH 32	Byte 4 Bit 7	MSN-6 Bit 8	31		
CH 31	Byte 4 Bit 6	MSN-6 Bit 4	10		
CH 30	Byte 4 Bit 5	MSN-6 Bit 2	52		
CH 29	Byte 4 Bit 4	MSN-6 Bit 1	32		
CH 28	Byte 4 Bit 3	MSN-7 Bit 8	11		
CH 27	Byte 4 Bit 2	MSN-7 Bit 4	53		
CH 26	Byte 4 Bit 1	MSN-7 Bit 2	33		
CH 25	Byte 4 Bit 0	MSN-7 Bit 1	12		



Indicates signals also used as the Questionable Register inputs

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TABLE 2-6 8063 EXAMPLE SIGNAL-PIN ASSIGNMENTS

Signal	Signal Weighting		Pin	User Signals	
	Binary	BCD/HEX		Pin	Signal
CH 8	Byte 1 Bit 7	MSN Bit 8	39		gnd = 0
CH 7	Byte 1 Bit 6	MSN Bit 4	18		gnd = 0
CH 6	Byte 1 Bit 5	MSN Bit 2	60	25	Status bit 2
CH 5	Byte 1 Bit 4	MSN Bit 1	40	26	Status bit 1
CH 4	Byte 1 Bit 3	MSN-1 Bit 8	19	27	Counter bit 4
CH 3	Byte 1 Bit 2	MSN-1 Bit 4	61	28	Counter bit 3
CH 2	Byte 1 Bit 1	MSN-1 Bit 2	41	29	Counter bit 2
CH 1	Byte 1 Bit 0	MSN-1 Bit 1	20	30	Counter bit 1
CH 16	Byte 2 Bit 7	MSN-2 Bit 8	15		+5 = 1
CH 15	Byte 2 Bit 6	MSN-2 Bit 4	57		gnd = 0
CH 14	Byte 2 Bit 5	MSN-2 Bit 2	37		+5 = 1
CH 13	Byte 2 Bit 4	MSN-2 Bit 1	16	1	Plus
CH 12	Byte 2 Bit 3	MSN-3 Bit 8	58	2	Overrange
CH 11	Byte 2 Bit 2	MSN-3 Bit 4	38		gnd = 0
CH 10	Byte 2 Bit 1	MSN-3 Bit 2	17		gnd = 0
CH 9	Byte 2 Bit 0	MSN-3 Bit 1	59	3	100s 1
CH 24	Byte 3 Bit 7	MSN-4 Bit 8	54	4	10s 8
CH 23	Byte 3 Bit 6	MSN-4 Bit 4	34	5	10s 4
CH 22	Byte 3 Bit 5	MSN-4 Bit 2	13	6	10s 2
CH 21	Byte 3 Bit 4	MSN-4 Bit 1	55	7	10s 1
CH 20	Byte 3 Bit 3	MSN-5 Bit 8	35	8	1s 8
CH 19	Byte 3 Bit 2	MSN-5 Bit 4	14	9	1s
CH 18	Byte 3 Bit 1	MSN-5 Bit 2	56	10	1s 2
CH 17	Byte 3 Bit 0	MSN-5 Bit 1	36	11	1s 1
CH 32	Byte 4 Bit 7	MSN-6 Bit 8	31	-	-
CH 31	Byte 4 Bit 6	MSN-6 Bit 4	10	-	-
CH 30	Byte 4 Bit 5	MSN-6 Bit 2	52	-	-
CH 29	Byte 4 Bit 4	MSN-6 Bit 1	32	-	-
CH 28	Byte 4 Bit 3	MSN-7 Bit 8	11	15	D/A bit 11
CH 27	Byte 4 Bit 2	MSN-7 Bit 4	53	16	D/A bit 10
CH 26	Byte 4 Bit 1	MSN-7 Bit 2	33	17	D/A bit 9
CH 25	Byte 4 Bit 0	MSN-7 Bit 1	12	18	D/A bit 8


 indicates signals also used as the Questionable Register inputs



TABLE 2-6 8063 EXAMPLE SIGNAL-PIN ASSIGNMENTS

Signal	Signal Weighting		Pin	User Signals	
	Binary	BCD/HEX		Pin	Signal
CH 40	Byte 5 Bit 7	MSN-8 Bit 8	7	19	D/A bit 7
CH 39	Byte 5 Bit 6	MSN-8 Bit 4	49	20	D/A bit 6
CH 38	Byte 5 Bit 5	MSN-8 Bit 2	29	21	D/A bit 5
CH 37	Byte 5 Bit 4	MSN-8 Bit 1	8	22	D/A bit 4
CH 36	Byte5 Bit 3	MSN-9 Bit 8	50	23	D/A bit 3
CH 35	Byte 5 Bit 2	MSN-9 Bit 4	30	24	D/A bit 2
CH 34	Byte 5 Bit 1	MSN-9 Bit 2	9	25	D/A bit 1
CH 33	Byte 5 Bit 0	MSN-9 Bit 1	51	26	D/A bit 0
CH 48	Byte 6 Bit 7	MSN-10 Bit 8	46		-
CH 47	Byte 6 Bit 6	MSN-10 Bit 4	26		-
CH 46	Byte 6 Bit 5	MSN-10 Bit 2	5	40	Control bit 2
CH 45	Byte 6 Bit 4	MSN-10 Bit 1	47	41	Control bit 1
CH 44	Byte6 Bit 3	LSN Bit 8	27		-
CH 43	Byte 6 Bit 2	LSN Bit 4	6	42	LED #3
CH 42	Byte 6 Bit 1	LSN Bit 2	48	43	LED #2
CH 41	Byte 6 Bit 0	LSN Bit 1	28	44	LED #1
Signal	Function	Pin			
EDR	External Data Ready Input	25	12	DPM Busy	
INH	Inhibit Signal Output	45		-	
Stat A	Status A Input	4	50	Ext +5 Vdc	
Stat B	Status B Input	44		gnd	
Trigger	Trigger Pulse Output	24		-	
Reset	Reset Pulse Output	3		-	
Remote	Remote State Output	43		-	
Clear	Clear Pulse Output	23	49	Ext Reset In	
Strobe	Data Output Strobe	2	27	D/A Load	
Vcc	+5 Vdc Output #	1		for +5V jumpers	
Gnd	Signal Ground	22		for gnd jumpers	
+12V	+12Vdc Unreg Input *	62		-	
Ret	Unreg Input Return *	42		-	

Notes: # Limit load to 100 mA, * for OEM use only

