

IEEE 488/GPIB BUS INTERFACES

4863 GPIB ↔ PARALLEL DIGITAL INTERFACE

DESCRIPTION

The 4863 is an IEEE-488/GPIB/HP-IB compatible Digital Interface that provides 48 user programmable digital lines that can be configured as inputs or outputs in eight bit bytes. Each data line has a pullup resistor for sensing contact closures or TTL/CMOS inputs. As outputs, each line is latched and can source 24 mA or sink up to 48 mA. The 4863 can also be configured to monitor up to 15 input lines for changes. Applications include interfacing devices with parallel digital signals to the GPIB bus, controlling discrete devices from the GPIB bus, monitoring digital signals for changes and transferring digital data.

The Model 4863 is a member of ICS's Minibox™ interface family. All Minibox™ interfaces are IEEE 488.2 compatible, use SCPI commands for ease of programming and are packaged in a CE compliant metal case that is less than 1.6 inches (39 mm) high. Rack mounting kits are available for mounting one or two Model 4863s in a single 1U high space.

Versatile Digital Interface

The 4863's digital interface is configured with commands from the GPIB Bus. The configuration commands permit the user to designate the data lines as inputs and/or outputs in 8-bit byte increments, connect bytes into strings, set data polarity, data format, and handshake modes. The user can set the output lines to his desired values and save the current configuration in the 4863's Flash memory. The saved configuration becomes the new power-on configuration. At power turn-on, the Digital I/O lines are initially tristated and then set to the saved configuration. A Stable signal is asserted after the digital I/O lines are configured to enable external logic or relays.



4863 Parallel Interface

Data Transfer Methods

Data transfer between the computer and the 4863's digital interface can be by individual bits, by bytes or as strings of data values to multiple bytes as shown in Figure 2.

- **Bit** commands set or reset specific bits in a byte or query a input bit in a byte. Pulse commands let the user pulse single or multiple output lines in one command.

- **Byte** commands set all 8 bits in a specific byte or read data from a byte.

- **String** transfer commands send strings of data characters to one or more output bytes to make a multi-byte output word or read a string of data from one or more input bytes. The user designates these bytes as inputs or outputs when he configures the unit.

The 4863 has three address modes. In the Single Address Mode, the user can set the 4863's configuration and transfer data as described above. In the Dual Primary or Single Primary-Dual Secondary Mode, the lower address responds to commands like the Single Address Mode. The upper address transparently passes strings of data to the configured output bytes or reads data from any configured input bytes. The data is sent or read without having to parse any commands, eliminating the parsing time and speeding up the data transfer.

Controlling the Digital Interface

Figure 2 shows the different ways the 4863 can be used to transfer data. The arrows show the data direction. The user can use SCPI commands or ICS's Short Form commands to control the digital interface. Short form commands are used in Figure 2 for brevity.

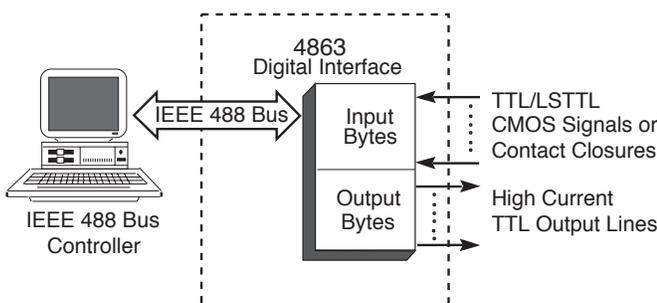


Figure 1 4863 Block Diagram

A flexible interface between the IEEE 488 Bus and devices with digital signals.

- Provides a user-definable, 48-line parallel interface with bit, byte, pulse, string and binary data transfer capabilities. *Fully configurable to the user's needs by bus commands.*
- Signal monitor feature allows the 4863 to detect signal changes on 15 inputs. *Relieves controller of time consuming polling function*
- High-current drivers and input pullup resistors. *Drives more devices, longer lines and inputs CMOS signals or switch contacts.*
- Device configuration and bus address stored in Flash. *Saves your setup values.*
- IEEE-488.2 compatible unit uses SCPI commands and Short Form commands. *Includes latest GPIB program advances.*
- Packaged in small 1U high metal case. *Smaller size with EMI/RFI protection.*
- Includes a menu-driven configuration program. *Steps user through configuration choices.*

CE Approved

RoHS

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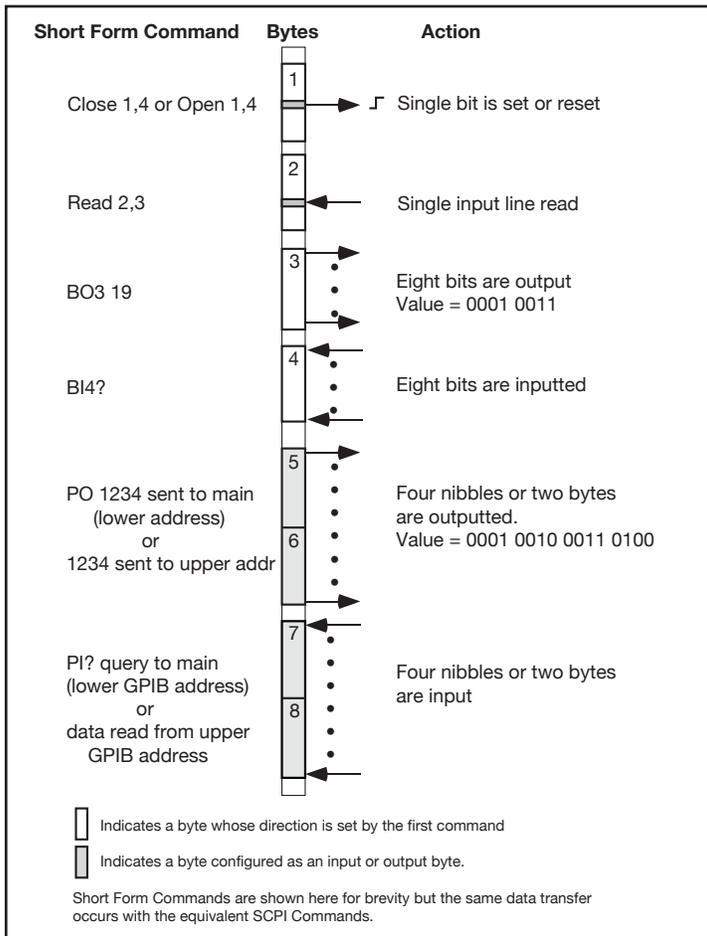


Figure 2 4863 Digital Transfer Methods

(Figure shows 8 bytes for illustrative purposes. Actual 4863 has 6 bytes)

Controlling the Digital Interface continued

The bit, byte and pulse commands automatically set the data direction for their data bytes. Bit command examples are Close, Open and Read which operate on bits in bytes 1 and 2. All eight bits in byte 3 are written by the BO3 command. Data strobes can be manually generated if needed. Pulse commands can be used to pulse an output bit to its logically on level. All eight bits in byte 4 are read by the BI4? query. The user can set the data polarity on a bit or byte basis for all of the bytes controlled by the bit and byte commands.

String commands can transfer 1 to n bytes of data at a time. String commands only work on data bytes that are pre-configured as input or output bytes by the CONFigure commands. The user can configure 1 to n bytes as inputs and/or as outputs to make data words up to 128-bits wide. Figure 2 shows two bytes (bytes 5 and 6) configured as outputs and two bytes (bytes 7 and 8) configured as inputs. All 16 bits in bytes 5 and 6 are output by the PO command. A data strobe is automatically generated when data is outputted by a string command. The PI? query reads 16 bits of data from bytes 7 and 8. The user can select the data format, data polarity, data strobe polarity and handshaking for the string commands.

The setup configuration and output data values can be saved at any time. The saved configuration and output values become the new power turn-on default values and are restored when the unit is reset or when power is turned on. This allows a user to set the output signal states at power turn-on time.

Binary Data Transfer

The binary data transfer mode can be used to quickly output large blocks of data to test devices. The user configures the desired output bytes, sets the listen format to binary and enables either dual address mode. The 4863 is then addressed at its upper GPIB address and the binary data is outputted to the 4863. The 4863 latches each bus character in a separate output byte and then pulses the data strobe when all bytes have been loaded. This sequence repeats until all of the data has been transferred.

Input Signal Monitoring

The 4863 can monitor up to fifteen of the digital inputs for signal changes and generate an SRQ to notify the Bus Controller when changes occur. Monitoring is done by setting the 4863's Questionable Transition register to detect positive and/or negative signal transitions and enabling bits in the Questionable Event register. When the enabled bit(s) are detected, the 4863 generates an SRQ to alert the Bus Controller to the event. The user can query the 4863's Questionable Registers to determine the input signal states and which signal(s) changed state. See Application Bulletin 48-18 for more details and a program example.

Controlling the 4863 with SCPI Commands

Figure 3 shows the 4863's configuration and data transfer commands as a SCPI Command Tree. Each SCPI command has a corresponding Short Form command which can be used for quick programming. Most of the functions can also be queried to verify the command setting. (i.e. IPn? reads back the byte's polarity setting)

The ROUTe Bit Commands let the user set/reset individual bits in an output byte and the SOURCE Output byte commands latch an 8-bit value into a specific output byte without pre-configuring the bytes. Data Strobes can be manually generated if needed. ROUTe: PULS or ROUTe:PULSe:CHAN commands pulse any output line. Pulse width is set by the ROUTe:PULSe:WIDTh command.

SOURCE Output string commands can send strings of data to bytes that have been pre-configured as outputs (with the CONFigure command) and generate a data strobe with a single command. The data format is controlled by the FORMat command. Transparent data transfer is possible in the Dual Address Mode where bytes from the GPIB bus are formatted and outputted to the previously configured output bytes.

SENSE Bit commands read the state of a specific bit in an input byte and the SENSE byte commands read data from a specific byte.

SENSE String Commands read data from bytes that have been pre-configured as input bytes by the CONFigure command. The data format on the GPIB bus is controlled by the FORMat command.

CALibrate Commands let the user personalize the 4863 with his own IDN string, lock settings to prevent changes and reset the unit to the factory settings.

4833 Compatibility

The 4863's transparent data transfer mode and short form configuration commands are similar to ICS's Model 4833 GPIB to Parallel Interfaces. Users familiar with the Model 4833 will be able to use many of the same commands and command syntax when configuring the 4863's interface.

4863 SCPI COMMAND TREE

SCPI Commands	Short Form Commands	
SYSTem		
:COMM		
:GPIB		
:ADDRESS	<numeric>	
:EXT		
:MODE	SINGle DUAL SEC	
:ERRor?		
:VERSion?		
CONFigure		
[:DIGital]		
:INPut	<channel list>	N
:POLarity	0 1	TPn
:HANDshake	<boolean>	TBn
:OUTput	<channel list>	LN
:POLarity	0 1	LPn
:HANDshake	<boolean>	LH
:CLEar	0 1	C
:EDR	0 1	E
:INHibit	0 1	I
:REMote	0 1	R
:RESet	0 1	X
:STRobe	0 1	S
:TRIGger	0 1	TR
:ASTATus	0 1	A
:BSTATus	0 1	B
FORmat		
:TALK	<ASCIi Hex HEXL Table>	
:TRANslation	<16 char string>	V
:LISTen	<ASCIi Hex HEXL BIN 4833>	
SENSe		
	Input	
:BIT?	byte,bit	READ?
:READ?	byte	BREAD?
[:DIGital]		
:DATA		
[:VALue]?		PI?
:PORT?	number or <channel list>	BI?
:PORTn?		Bln?
:POLarity?		IPn
:RESet:EDR		ER
ROUTe		
	Bit Comds	
:CLOSe	byte,bit	CLOSE
:OPEN	byte,bit	OPEN
:RESEt	byte	BRESEt
:PULSe	byte,bit	PL
:CHANnel	number or channel list	PC
:WIDTh	10-30000 [50]	PW
[SOURce]		
[:DIGital]		
:DATA		
[:VALue]	0-255	PO
:PORTn	0-255	BOn
:POLarity	0-255	OPn
:STRobe		SP
CALibrate		
	Calibrate Configuration	
:IDN	string (72 char max)	
:DATE	mm/dd/yy	
:DEFault		
:LOCK	1(On) 0(Off) [0]	

Figure 3 4863 SCPI Command Tree

Digital IO Connections

All of the 4863's Digital I/O signals are brought out on a 62-pin connector on the rear panel of the 4863. There are several ways to connect to the 4863:

Each 4863 includes a 62-pin mating connector with a shell. The user can solder 24 to 28 AWG wires to the mating connector to connect to his digital devices or to another connector.

The optional 114508 Open-end Cable is a five foot long, round, open-end cable with 28 AWG wires. The cable wires can also be soldered directly to a PC board or to a connector that mates to another device.

The optional 114714-004 Digital I/O Extension Cable with male connectors provides a four foot long, pin-to-pin extension of the 4863's Digital I/O signals. Mount a female connector (P/N 902268) on the mating PC board to complete the signal connection.

OEM Board Version

The 4863 is also available as a board version for OEM applications. Board versions are designed to be mounted in the host's chassis and are powered from the host's 7 to 32 volt power supply. The boards have both GPIB and RS-232 and RS-485 Serial interfaces. On the OEM boards, the GPIB and Serial interface headers are mounted vertically to minimize the board footprint. The Digital I/O connector is the same right angle 62-pin connector with lock studs as is used on the 4863 enclosed units. The 26-pin GPIB header mates with ICS's GPIB Connector/Address Switch Cable Assemblies and includes input lines for a rear-panel mounted address switch. A SCPI command enables/disables the external switch inputs. The OEM boards include the mating digital connector and a serial cable kit with a plug that mates with the serial header.

GPIB Address

Enclosed 4863's store their GPIB address in Flash memory and use a SCPI command to change it. On OEM boards, extra sense lines are provided to read the GPIB address from an external address switch at power turn-on time. The SCPI command lets the user enable external address switch reading or disable the external switch sensing to use the internal flash setting.

GPIB Header

On OEM boards, the 4863's GPIB connector is replaced with a 26-pin vertical header for remoting the GPIB bus and address switch signals to the rear panel. The 26 pin header mates with a flat ribbon cable from ICS's GPIB Connector/Address switch assemblies. These compact, business card size assemblies provide a convenient way to mount a GPIB Connector and an address switch on the rear panel.

RS-232/RS-485 Interfaces

OEM board versions include an RS-232/RS-485 serial interface. The serial interface operates at rates up to 115,200 baud. The serial interface provides all of the functionality of the GPIB interface but allows for control of the 4863 by any PC's COM port or by an RS-485 network. Up to sixteen 4863s can be placed on a single RS-485 network. The 4863s are addressed by a two character address

4863: APPLICATIONS

sequence that proceeds the actual command. When the OEM board has both interfaces, the unit defaults to the serial interface until the GPIB interface enters the Remote state.

LED Header

An 8 pin header on the OEM boards allows easy extension of the 4863's LEDs to the user's front panel.

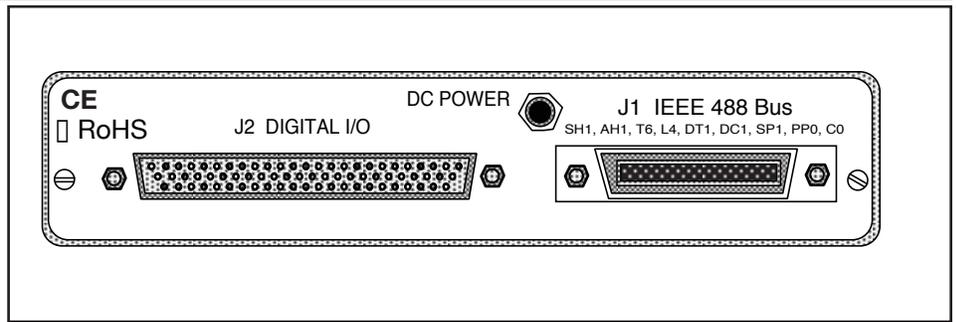


Figure 4 4863 Rear Panel

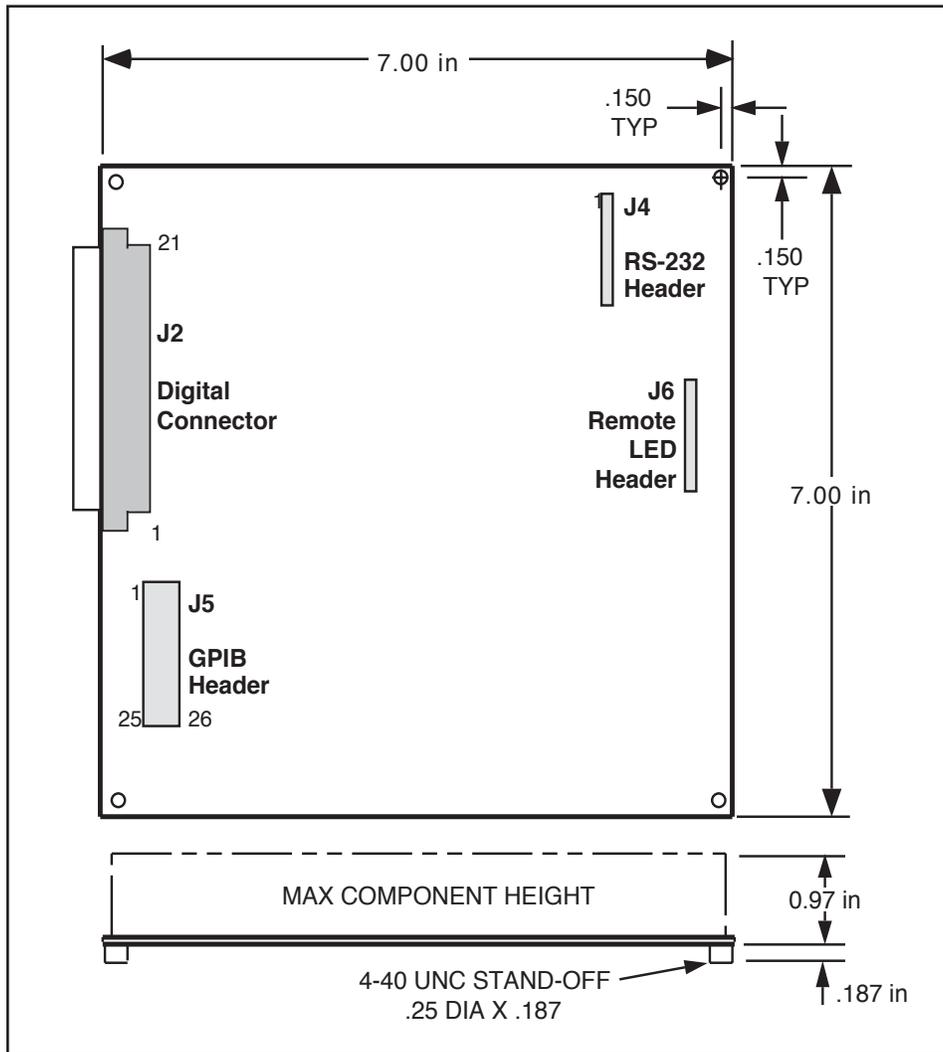


Figure 5 4863 OEM Board Dimensions

TABLE 1 4863 OEM BOARD CONFIGURATIONS

Part Number	Interfaces		
	GPIB	RS-232	RS-485
114514	Yes	No	No
114515	Yes	Yes	Yes
114711	No	Yes	Yes

OEM 4863 Boards include Instruction Manual, Support CD, mating digital I/O connector and hood and a serial cable connection kit. GPIB Connector/Switch Assemblies and other cables are ordered separately.

OEM Customization

The 4863's firmware allows the user to store an IDN message and other setup parameters in the 4863's Flash memory. This effectively integrates the board into the user's system and makes the 4863 appear as part of the OEM's product. A lock function hides the setup variables from the end user and prevents accidental changes to the setup.

4863: SPECIFICATIONS

IEEE 488 Bus Interface

The 4863's 488 Bus interface meets IEEE STD 488.1-1987 and has the following capabilities:

SH1, AH1, T6, L4, SR1, PP0, DC1, RL0, DT1, C0 and E2 drivers.

Address Capability

Dual primary addresses or single primary with secondary addresses 00 and 01. Primary address range: 0-30.

SRQ Generation

SRQs are generated if the unit is not a talker, if SRQs are enabled and if an Enabled Event Status Register bit or an monitored digital input change occurs. Digital inputs monitored by the Questionable registers.

488.2 Common Commands

*CLS, *ESE, *ESE?, *ESR?, *IDN?, *OPC, *OPC?, *RCL, *RST, *SAV, *SRE, *SRE?, *STB, TST? and *WAI

SCPI Commands

Used to set and query all programmable functions. The 4863 conforms to SCPI 1995.0 Specification.

Table 2 Programmable Functions

GPIB Bus Address
Address mode
Number of Talk bytes
Input Signal polarity
Input Handshaking
Talk data format
Talk data conversion table
Number of Listen bytes
Output Polarity
Output Handshaking
Listen data format
Control line polarities
SENSe input polarities
SOURce output polarities
STATus register configurations

Signal Characteristics

The 4863's parallel I/O signals have the following electrical characteristics. All time delays listed here are maximums, all pulse widths are minimums.

Lines 48 Digital I/O plus
2 Status Inputs
Input High => +2.0 V @ ±10 μA
Logic Low = <0.8 V @ 250 μA
Levels with 33 Kohm pullup to +5 Vdc for sensing contacts.
Max High = 5.5 V

Input External Data Inhibit line
Timing SETS within 1 μs of the active edge of the EDR Input signal and resets after data is loaded. Data loading time for 6 BCD/HEX characters is 0.15 ms (typ.) after the 4863 has been addressed as a Talker

Output High => 3 V with 3 mA source
Logic High => 2 V with 24 mA source
Levels Low = 0.0 to +0.55 Vdc, 48 mA sink

Output Data is transferred to the
Timing output 0.6 to 5.3 ms after receipt of a terminator depending upon transfer method.

Pulse 10 to 30000 ms in 10 ms steps
Width 50 ms default

Data Stb Output pulse width, 5 μs
Trigger Output pulse width, 5 μs
Clear Output pulse width, 5 μs
Reset Output pulse width, 40 μs for *RST command and true during 4863 reset time (70 ms)

Controls and Indicators

POWER Front-panel switch

LEDs

PWR Indicates power on
RDY Unit has passed self test
TALK Unit is addressed to talk
LSTN Unit is addressed to listen
SRQ Unit is asserting SRQ
ERR Unit has detected a command error

Physical

Size W x H x D
7.29 x 7.45 x 1.52 inches
(118.52 x 189.2 x 38.6 mm)

Weight 3 lbs (1.4 kg)

Temperature

-10°C to +55°C Operating
-40°C to +70°C Storage

Humidity 0-90% RH no condensation

RFI/EMI CE Certified

Connectors and Headers

IEEE bus: Std 24-pin metal shell w/ metric studs
I/O: 62-pin female, metal DC shell connector with lock studs
Serial: 10-pin male header
LEDs: 8-pin male header

Power 9 to 32 Vdc @ 3 VA

Included Accessories

Instruction Manual
Mating Connector and Hood
CD-ROM with GPIB Keyboard Controller program and Configuration Utility.
UL/CSA/VDE approved AC power Adapters provided for:
US 115±10% Vac, 60 Hz (std, no dash)
-U Universal 100-230 Vac, 50/60 Hz with plugs for Australia, China, Europe, Japan, UK and US.

ORDERING INFORMATION

	Part Number
IEEE 488 Bus Parallel Digital Interface with 115 VAC adapter	4863
IEEE 488 Bus Parallel Digital Interface with 100-230 VAC adapter.	4863-U
OEM IEEE 488 Bus to Parallel Digital Interface Boards (Pwr adapter not included. See Table 1 for accessories)	See Table 1
OEM IEEE 488 Bus to Parallel Digital Interface Board Starter Kit	114499